

<b>Application Number</b>  <b>*10635811*</b>	<b>Application No.</b> 10/635,811	<b>Applicant(s)</b> PARK ET AL.	

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**TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING REJECTION OVER A PRIOR PATENT**

Docket Number (Optional)

51876P362

TERMINAL DISCLAIMER  
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In re the Application of: Jong-Bum Park, et al.

Application No.: 10/635,811

Filed: August 05, 2003

For: method for fabricating capacitor in semiconductor device

AUG 23 2004

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The owner\*, Hynix Semiconductor Inc. of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 5,670,431.

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Based on PTO/SB/26 (09-03) as modified by Blakely, Solokoff, Taylor & Zetman (w/r) 09/17/2003.  
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